## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Derick G. Behrends et al.

Serial No. : 10/636,060

Filed: August 7, 2003

For : METHODS AND APPARATUS FOR TESTING INTEGRATED

CIRCUITS

Examiner : James C. Kerveros

Group Art Unit : 2117

Customer No. : 46628

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

## AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Sir:

In response to the Final Office Action mailed June 20, 2007 Applicants respectfully request entry of the following amendments and remarks into the official record of the above-identified application.

Amendments to the Claims begin on page 2.

Remarks begin on page 8.